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Ch. 1 -

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1 Exercise

Solutions. 1.1.

There are 14

nodes in the

circuit. Thus,

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there are  $14 \times 2 = 28$  single stuck-at faults. For multiple stuck-at fault, it has  $(2 + 1)14 = 42$  multiple stuck-at faults.

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$$S_i = C_i + A_i + B_i,$$

$$C_{i+1} = C_i (A_i +$$



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Ch. 2 – Design  
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(Ad Hoc

Technique) Fig.

7: An example of  
a combinational  
feedback loop in  
a combinational  
circuit. Figure

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7 shows an example of a combinational feedback loop. In this example, when  $A = 1$ ,  $B = 0$ , and  $C = 1$ , the circuit acts as an oscillator, which prevents us from being able to predict a valid value at

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Chapter 2

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Solutions 2.1

(Testability  
Analysis) Fig.

1: The SCOAP  
controllability  
and

observability  
measures for a  
3-input XOR gate

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1/3. Chapter 6

Exercise

Solutions: 6.1.

The complete dictionary at least includes the following five entries:

0000 0110 0100

0001 1100. The

five 4-bit

entries can be

encoded into

five 3-bit

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entries. The

compression

ratio is:

$$(1 - 3/4) * 100\% =$$

25%. 6.2.

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