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Stephen Brown and Zvonko Vranesic, Fundamentals of Digital

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The standard representation of these three logic building blocks is shown in Figure C.2.1. Rather than draw inverters explicitly, a common practice is to add “ bubbles ” to the inputs or outputs of a gate to cause the logic value on that input line or output line to be inverted. For example, Figure C.2.2 shows the logic diagram for the function

## Appendix C The Basics of Logic Design

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